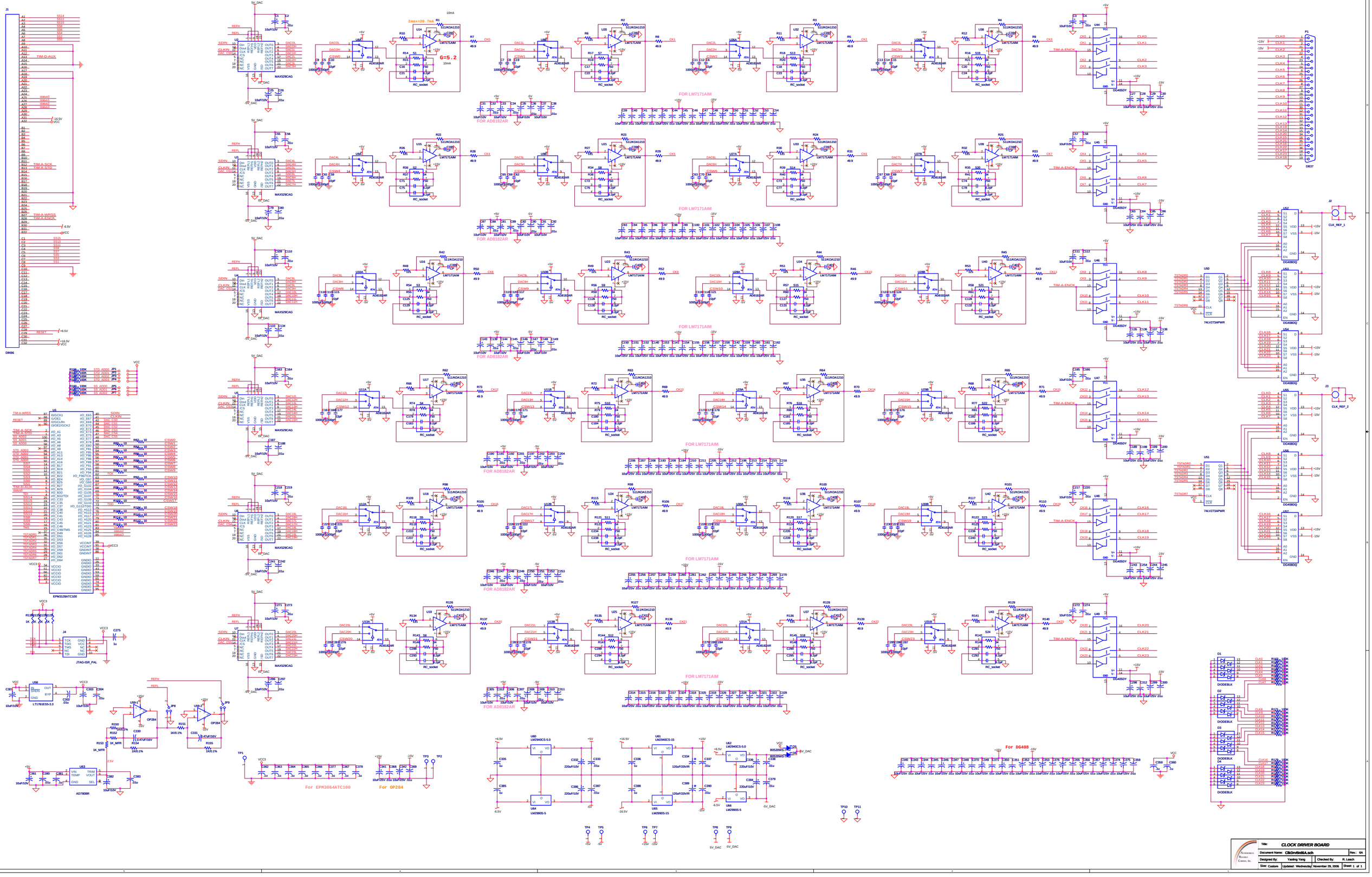
J1
DIN96
TIM-D-AUX
TIM-A-SCK
TIM-A-STD
TIM-A-WRSS
TIM-A-ENCK
RESET
+6.5V
+16.5V
VCC
-6.5V
5V_DAC
-5V_DAC
REFH
REFL
SDIN
CLKIN
DAC_CS14
MAX529CAG
10mA
Tmax=20.7mA
G=5.2
RC_socket
AD8182AR
LM7171AIM
FOR LM7171AIM
FOR AD8182AR
DG405DY
74LV273APWR
DIODEBLK
CLK_REF_3
CLK_REF_2
DB37
EPM3128ATC100
JTAG-ISR_PAL
LT1761ES5-3.3
OP284
AD7808R
For EPM3064ATC100
For OP284
For DG408
LM2940CS-5.0
LM2990S-5
LM2940CS-15
LM2990S-15
BOS20WS-TR
BOS20WS-TR
Title: CLOCK DRIVER BOARD
Document Name: ClkDrvBrdBA.sch
Rev.: 6A
Designed By: Yaoling Yang
Checked By: R. Leach
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Sheet: 1 of 1